

**Amendments to the Specification:**

Please replace paragraph the second full paragraph on page 7, spanning lines 12-20, with the following rewritten paragraph:

---

Q1 The FPCs 224 may include input/output control logic that directs and manages the packets received and transmitted by the PICs 222. The FPCs 224 may, for example, count packets and bytes, apply class-of-service rules to packets, prioritize packets, and perform basic packet integrity checks. The SFMs 226 may include logic that determines how to route the packets. The SFMs 226 may operate upon packet header information received from the FPCs 224 to identify the PIC 222 to output the packet. The SFMs may use the a forwarding table 228 to make this determination. The forwarding table 228 may obtain a copy of the forwarding table 214 from the routing engine 210.

---

Please replace paragraph the first full paragraph on page 9, spanning lines 11-19, with the following rewritten paragraph:

---

Q2 Two wire serial sub-bus 420 connects master control processor 407, through midplane 310, to switch 430 of boards 402-405. Control information used to facilitate the operation of two wire serial bus 420 at circuit boards 402-405 is similarly routed over midplane 310 and is received by local control logic circuits 432. The operation of two wire serial bus 420 in circuit boards 402-405 will be described in more detail below. Switch 430 and local control logic 432 may similarly be implemented on circuit board 401. In this situation, serial sub-bus 424 417 may optionally be merged with sub-bus 420, and circuit board 401 can appear functionally like circuit boards 402-405.

---